IFW

Docket No.: 071971-0015

NOV 3 0 2005

PATENT

IN THE STATES PATENT AND TRADEMARK OFFICE

In re Application of

Customer Number: 20277

Shiro SAKIYAMA, et al.

Confirmation Number: 6689

Application No.: 10/511,165

Group Art Unit: 2816

Filed: October 14, 2004

Examiner: Terry D. Cunningham

For: SEMICONDUCTOR INTEGRATED CIRCUIT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith is an Amendment in the above-identified application.

No additional fee is required.

Applicant is entitled to small entity status under 37 CFR 1.27

Also attached:

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	FEE
Total Claims	18	20	0	\$50.00 =	\$0.00
Independent Claims	3	3	0	\$200.00 =	\$0.00
		Multiple dependent claims newly presented			\$0.00
	Fee for extension of time			\$0.00	
					\$0.00
		Total of Above Calculations			\$0.00

Please charge my Deposit Account No. <u>500417</u> in the amount of \$0.00. An additional copy of this transmittal sheet is submitted herewith.

The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any overpayment, to Deposit Account No. 500417, including any filing fees under 37 CFR 1.16 for presentation of extra claims and any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

McDERMONT WILL & EMERY LLP

Michael E. Fogarty Registration No. 36,139

Please recognize our Customer No. 20277 as our correspondence address.

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Date: November 30, 2005

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AMENDMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated September 1, 2005, having a three-month shortened statutory period for response set to expire December 1, 2005, please amend the above-identified application as follows:

Application No.: 10/511,165

IN THE TITLE:

Please replace the existing title with the following new title:

SEMICONDUCTOR INTEGRATED CIRCUIT FOR CONTROLLING THE

SATURATION CURRENT OF MOS TRANSISTORS